

CONGRATULATIONS TO THE VOICE 2008 BEST PAPER WINNERS!

Verigy's annual user group meeting, VOICE, took place September 23-24 in San Jose, Calif. Congratulations to the VOICE 2008 Best Paper winners – click on the paper title to read the abstract.

Best Papers, by Track

Communications & RF Track

[A Fast and Accurate Method to Perform Evaluation Board to Production Board Correlation by Using Non-linear Device Characteristics](#)

Michael Engelhardt, Verigy

Memory Track

[Mass Production Test Solution for XDR 4.8Gbps on the Leading Edge of DRAM](#)

Kunihiko Kato, Elpida * Takeshi Sonoda, Verigy

Test Techniques and Methodologies Track

[Highly Parallel DC measurements for Fab In-Line Monitoring](#)

Ernesto Shiling, IBM * Lou Medina, IBM * Bob Smith, Verigy

Productivity Software and Tools Track

[Getting More out of SmarTest Eclipse Work Center](#)

Michael Vogt, Verigy * Michael Kozma, Verigy * Chris Buoy, Verigy

DC and Mixed Signal Track

[Revolutionary Dynamic DC Measurement Using DC Scale](#)

Aether Lee, Verigy * Takashi Shibata, Verigy

High-speed Digital Interfaces Track

[Functional Testing of PCI-Express using Pin Scale 3600](#)

Steve Karako, Freescale Semiconductors * Michael Kozma, Verigy * Barry Schmidt, Verigy

Runners Up

The competition was close. These additional papers scored very close to the winning papers and also deserve congratulations:

High-speed Digital Interfaces Track – Runners Up

A Time Domain Reflectometry Kit for ATE Test Fixtures

https://www.verigy.com/content/dam/verigy/Internet/Voice/2008/abstracts/hsdi/hdi_44.pdf

Jose Moreira, Verigy * Heidi Barnes, Verigy * Callum McCowan, Verigy * Ross Winters, Intel

Novel Clock Signal Analyses with a Waveform Sampler – Rise/Fall Time, Jitter and Jitter Separation (HS)

https://www.verigy.com/content/dav/verigy/Internet/Voice/2008/abstracts/hsdi/hdi_21.pdf

Hideo Okawara, Verigy

Memory Track – Runner Up

GDDR5 Test Challenges and their Solutions on the V93000 HSM Series (M)

https://www.verigy.com/content/dav/verigy/Internet/Voice/2008/abstracts/memory/mem_66.pdf

Hubert Werkmann, Verigy * Seo Ji-Won, Verigy * Jin Han-Ho, Hynix * Kang Hee-Won, Hynix

Test Techniques and Methodologies Track – Runners Up

Demystifying Digital Capture (TTM)

https://www.verigy.com/content/dav/verigy/Internet/Voice/2008/abstracts/test_tech/tt_133.pdf

Amit Monga, Verigy * Daniel Simoncelli, Verigy * Michael Kozma, Verigy

Unified Volume Diagnostics for ATE and In-System Logic BIST Using the Inovys Scan Imager Solution (TTM)

https://www.verigy.com/content/dav/verigy/Internet/Voice/2008/abstracts/test_tech/tt_113.pdf

Douglas Kay, Cisco * Lien Tran, Cisco * Matthias Kamm, Cisco

Proceedings CD

Did you miss VOICE 2008? If so, now is your chance to buy the proceedings CD with all the papers presented. The CD is \$50 to purchase, and there is a limit of 5. [Click here](#) to order.

Every VOICE paper is © Verigy 2008.

Watch for an expanded story on VOICE in the next issue of go/semi!